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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/700,940	11/21/2000	Shiro Sakiyama	10873.589USW	4531
75	590 05/19/2004		EXAM	INER
Merchant & Gould			EVERHART, CARIDAD	
PO Box 2903 Minneapolis, MN 55402-0903			ART UNIT	PAPER NUMBER
			2825	
			DATE MAILED: 05/19/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
Advisory Action	09/700,940	SAKIYAMA ET AL.			
•	Examin r	Art Unit			
	Caridad M. Everhart	2825			
The MAILING DATE of this communication appe	ars on the cover she t with th	orrespondenc address			
THE REPLY FILED 13 April 2004 FAILS TO PLACE TH Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appea Examination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this applic 1) a timely filed amendment whi	cation. A proper reply to a ch places the application in			
PERIOD FOR RE	PLY [check either a) or b)]				
a) The period for reply expires 3 months from the mailing date of	the final rejection.				
b) The period for reply expires on: (1) the mailing date of this Advevent, however, will the statutory period for reply expire later the ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The dathave been filed is the date for purposes of determining the period of extensions of the state of the shortened (b) above, if checked. Any reply received by the Office later than three moterned patent term adjustment. See 37 CFR 1.704(b).	an SIX MONTHS from the mailing date of FILED WITHIN TWO MONTHS OF THI te on which the petition under 37 CFR 1.1 sion and the corresponding amount of the statutory period for reply originally set in	f the final rejection. E FINAL REJECTION. See MPEP 36(a) and the appropriate extension fee the. The appropriate extension fee under the final Office action; or (2) as set forth in			
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CF					
2. The proposed amendment(s) will not be entered be	ecause:				
(a) X they raise new issues that would require furth	er consideration and/or search (see NOTE below):			
(b) ☐ they raise the issue of new matter (see Note by	•	,,			
(c) they are not deemed to place the application i issues for appeal; and/or	• •	erially reducing or simplifying the			
(d) they present additional claims without cancel	ing a corresponding number of	finally rejected claims.			
NOTE: See Continuation Sheet.					
3. Applicant's reply has overcome the following rejection	tion(s):				
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a s	eparate, timely filed amendment			
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request fo application in condition for allowance because:		sidered but does NOT place the			
6. The affidavit or exhibit will NOT be considered becaraised by the Examiner in the final rejection.	cause it is not directed SOLELY	to issues which were newly			
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we	c(s) a)□ will not be entered or bould be rejected is provided belo)□ will be entered and an ow or appended.			
The status of the claim(s) is (or will be) as follows:					
Claim(s) allowed:					
Claim(s) objected to:					
Claim(s) rejected: 1-6.					
Claim(s) withdrawn from consideration:					
B.☐ The drawing correction filed on is a)☐ approved or b)☐ disapproved by the Examiner.					
9. ☐ Note the attached Information Disclosure Stateme	• • • • • •				
10. Other:	(5)('			
TO Other					

Continuation Sheet (PTOL-303) 09/700,940

Applicati n No.

Continuation of 2. NOTE: Applicant's amendment "being determined based on" and and ""which is used to determine the capacitance value" and applicant's arguments based on the amendment would require further consideration, as this limitation was not in the finally rejected claims. Applicant has argued that Kinoshita fails to disclose arranging the power supply capacitor cell in a vicinity of the logic gate cell which is used to determine the capacitance value. Kinoshita was shown to disclose capacitor cells and logic cells in the lase Office Action, as Kinoshita places the capacitor cells between the logic cells. The bypass capacitors are power supply capacitors(col. 15 lines 1-11 of Kinoshita indicated that the unit capacitance cells function as feedthrough capacitors and bypass capacitors) and the capacitors are arranged in cells, as pointed out in the last Office Action. With respect to the value of the capacitance being determined, the combination of references was relied upon to show that it is known in the art to provide the recited capacitance values, and therefore it would have been obvious to one of ordinary skill in the art to have used these values, which were known in the prior art to provide optimum values.

C. Luethast Caridad Evernatt Primary Examinen